

A Theoretical Model of a Voltage Controlled Oscillator

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Introduction

Motivation

- The voltage controlled oscillator of the PLL has been modeled as a simple integrator, it does not capture the essence of dynamics of oscillation
- Oscillator instability issue critical for communication systems
- Need a high speed clock for ultra-wideband (UWB) communication systems
- Synchronization analysis for UWB systems

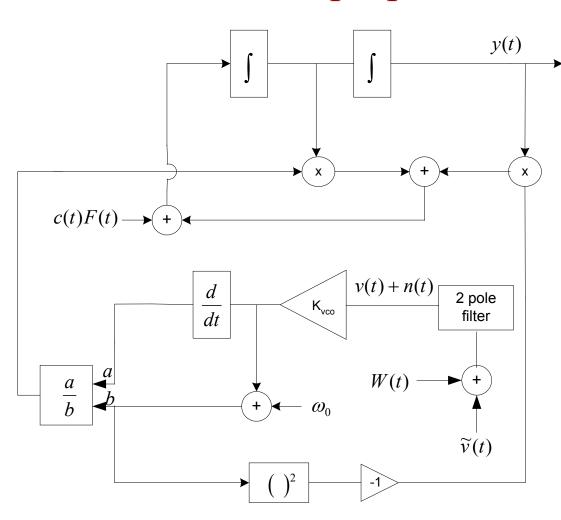
Problem statement

 Need a mathematical model of VCO that fully incorporates a means of non-linearity for stabilizing oscillators





Schematic of the proposed VCO with noise



F(t): internal noise of the clock

Z(t)

n(t): the tracking loop plus the controller noise

v(t): the controlled voltage

y(t): the oscillator waveform

 $\tilde{v}(t)$: input signal from the tracking loop

W(t): input noise from the tracking loop





Theoretical model

The voltage controlled oscillator can be described by the following stochastic differential equation which includes the internal noise, F(t), of the clock and the tracking loop plus the controller noise, n(t).

$$\ddot{y} - \frac{K_{\text{vco}}(\dot{v} + \dot{n}(t))}{\omega_0 + K_{\text{vco}}(v + n(t))} \dot{y} + \left[\omega_0 + K_{\text{vco}}(v + n(t))\right]^2 y = c(t)F(t)$$

where F(t) is assumed to be a white Gaussian noise, independent of n(t). In addition, $n(t) \in C^2([0,\infty))$, y(t) is the oscillator waveform, v(t) is the controlled voltage, c(t) is a scaled factor, ω_0 is the clock rest frequency $(\omega_0 > 0)$, and K_{vco} is the VCO gain.

• The output after the hard-limiter is $Z(t) = \operatorname{sgn}(v(t))$





Analytic solutions of the model

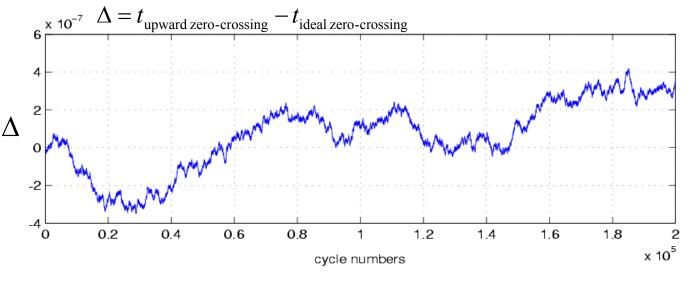
- Initial conditions: y(0) = 0, v(0) = 0, $y_1(0) = 0$, $y_2(0) = \hat{a}\omega_{\text{new}}$, $\hat{a} \in \mathbb{R}$
- When $v(t) = \begin{cases} at & if & t \le t_s \\ C & if & t > t_s \end{cases}$ where a, C are constants, and t_s is the time of steady state, unless otherwise specified.

	Analytic solution	Phase noise	Statistical properties
No noise present	$y_1(t) = \sin(\omega_0 t + \int_0^t K_{\text{vco}} a \tau \ d\tau)$	N/A	N/A
Internal noise $\widetilde{F}(t)$ present	$y_1(t) = \hat{a} \sin(\omega_{\text{new}} t) + \int_0^t c(s) \frac{\sin(\omega_{\text{new}} (t-s))}{\omega_{\text{new}}} dB_s$ where $c(s)$ is the scaling factor for the noise $F(t)$, $v(t) = a \ \forall \ t, \omega_{\text{new}} = \omega_0 + K_{\text{vco}} a, \text{ and } B_s \text{ is a 1-dimensiona 1}$ Brownian motion	$n_1(t) = K \cos \omega_{\text{new}} s \ dB_s$	$E[\varphi(t)] \approx \frac{c^2}{4\omega_{\text{new}}^3} \left[\cos(2\omega_{\text{new}}t) - 1\right]$ $R_{\varphi}(t,s) \approx R_{n_2}(t,s)$ $= \frac{c^2}{2\omega_{\text{new}}^2} \left[\min(s,t) - \frac{\sin(2\omega_{\text{new}}\min(s,t))}{2\omega_{\text{new}}}\right]$
Internal noise and external noise present		$\widetilde{\varphi}(t) = \tan^{-1}\left(\frac{\widetilde{n}_2(t)}{1 + \widetilde{n}_1(t)}\right)$ $\widetilde{n}_1(t) = \int_0^t \widetilde{K}(s, n(s)) \cos \Phi_{\text{new}}(s) dB_s$ $\widetilde{n}_2(t) = \int_0^t \widetilde{K}(s, n(s)) \sin \Phi_{\text{new}}(s) dB_s$ $\widetilde{K}(s, n(s)) = \frac{c}{\omega_0 + K_{\text{vco}}(as + n(s))}$ $\varphi_n(t) = \int_0^t K_{\text{vco}} n(z) dz$	$E[\widetilde{\varphi}(t)]$ and $R_{\widetilde{\varphi}}(t,s)$ are both time-varying and no closed form solution can be obtained. $R_{\varphi_n}(t,s) = \int_0^t \int_0^s K_{\text{vco}}^2 R_n(\tau,z) dz d\tau$

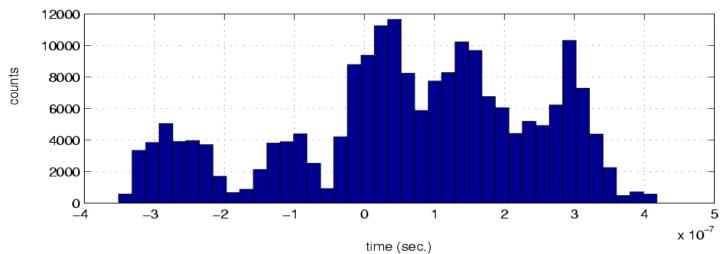


Results to date: simulation results internal noise only

Averaged upward zero-crossing jitter



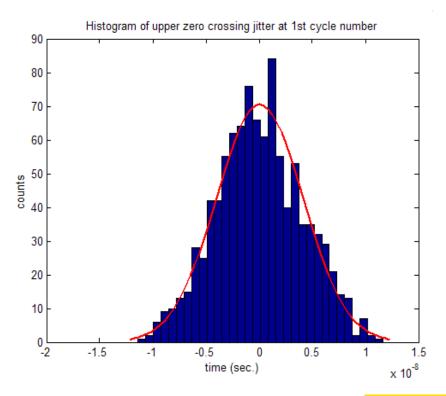
Simulation done at VCO frequency of 2 kHz and $\tilde{F}(t)$ is on.

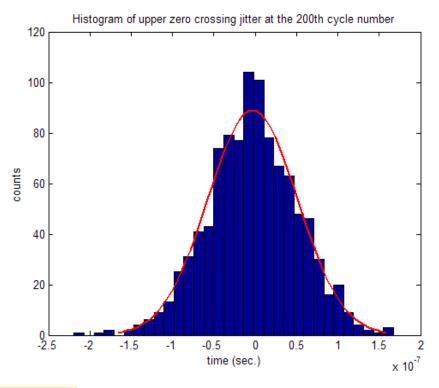




Simulation results

Histograms of upward zero crossing jitter at different time instants, 1000 runs.





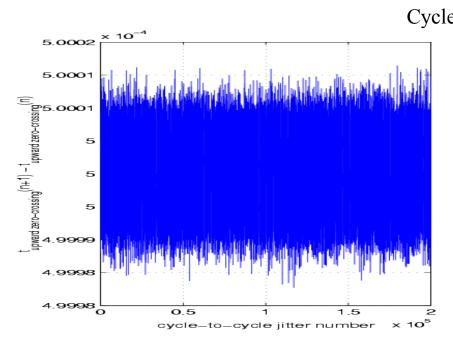
Note: Diffusion occurs

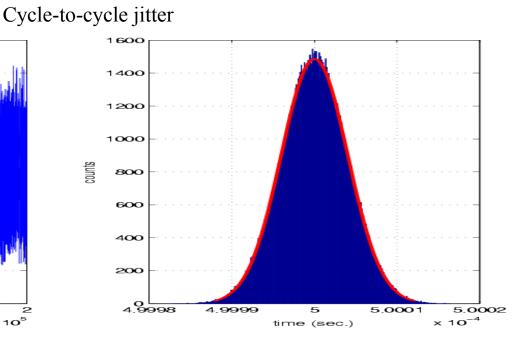




Simulation results

Cycle to cycle jitter simulated for 100 sec.





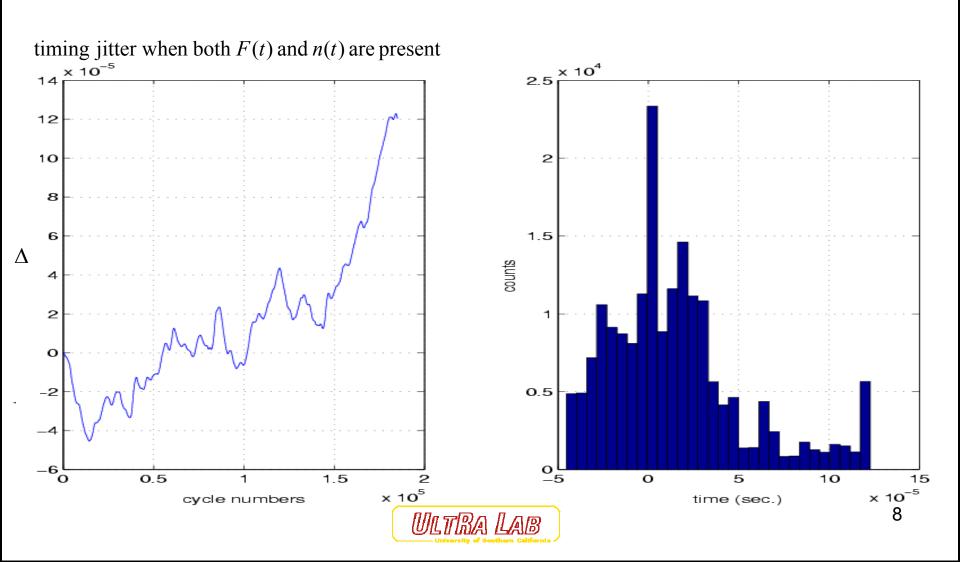
- Chi-squared goodness-of-fit test is used to test the normality of the test statistics
- The parametric Pearson correlation test is performed to see that the cycle to cycle jitters are independent.

ULTRA LAB

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Simulation results for internal + external noises





Summary of results

- A theoretical model of a voltage controlled oscillator is proposed based on physical dynamics with noise.
- Analysis of the resulting phase noise along with the simulation suggest that the timing jitter process has a random walk behavior (with restoring force) and the upward zero crossing jitter is normal distributed.
- The increment of the timing jitter process, cycle-to-cycle jitter, is shown to behave as normal distributed and independent.
- Results suggest that the tracking loop plus the controller noise n(t) cause the oscillator phase to drift while the internal noise $\widetilde{F}(t)$ tends to cause diffusion on the oscillator phase.

